Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	0	processor same (static or SRAM) same input same output same (energize or power) same couple$ same bridg$ same light	USPAT	OR	OFF	2005/02/15 14:34
L3	242	multimedia network processor (static or SRAM) input output (energize or power) couple$ bridg$ light	USPAT	AND	OFF	2005/02/15 14:37
L4	0	(multimedia network processor (static or SRAM) input output (energize or power) couple$ bridg$ light).ab.	USPAT	AND	OFF	2005/02/15 14:38
L5	0	(multimedia network processor (memory) input output (energize or power) couple$ bridg$ light).ab.	USPAT	AND	OFF	2005/02/15 15:37
L6	1	"045077"	US-PGPUB	AND	OFF	2005/02/15 15:47
L7	0	((processor and memory and (static adj memory) and (i/o adj device) and driver and (coupler$ same bridge$)))	US-PGPUB	AND	ON	2005/02/15 15:48
L8	0	((processor and memory and (static adj memory) and (i/o adj device) and driver and (coupler$ same bridge$)))	USPAT	AND	ON	2005/02/15 15:48
L9	4	((processor and memory and (memory) and (i/o adj device) and driver and (coupler$ same bridge$)))	USPAT	AND	ON	2005/02/15 15:50
L10	4	((memory and (i/o adj device) and driver and (coupler$ same bridge$)))	USPAT	AND	ON	2005/02/15 15:51
L11	123	((memory and driver and (coupler$ same bridge$)))	USPAT	AND	ON	2005/02/15 15:51
L12	4	((sram and driver and (coupler$ same bridge$)))	USPAT	AND	ON	2005/02/15 15:51
L13	2	((sram and ide and (coupler$ same bridge$)))	USPAT	AND	ON	2005/02/15 15:52
L14	4	(sram and ide and coupler$ and bridge$)	USPAT	AND	ON	2005/02/15 15:53
L15	11132	(memory same led)	USPAT	AND	ON	2005/02/15 15:53
L16	789	(memory same led) and sram	USPAT	AND	ON	2005/02/15 15:54
L17	51	(memory same led) and sram and ide	USPAT	AND	ON	2005/02/15 15:58
L18	50	(memory same led) and sram and ide and interface	USPAT	AND	ON	2005/02/15 15:54

L19	0	(memory same led) and sram and ide and bridge and coupler$	USPAT	AND	ON	2005/02/15 15:59
L20	0	(memory same led) and sram and ide and coupler$	USPAT	AND	ON	2005/02/15 15:59
L21	0	(memory) and sram and ide and led and coupler and bridge	USPAT	AND	ON	2005/02/15 16:00
L22	3	memory and ide and led and coupler and bridge	USPAT	AND	ON	2005/02/15 16:01